

## ECS Configuration Change Request

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Page(s)

1. Originator Robert Cole	2. Log Date: 5/9/02	3. CCR #: 02-0385	4. Rev:	5. Tel: 301-925-0799	6. Rm #: 2110C	7. Dept. SE/HW
8. CCR Title: Install CPU board in I0dis01 to support Solaris 8 transition						
9. Originator Signature/Date Robert Cole /s/ 5/7/02			10. Class II	11. Type: CCR	12. Need Date: 5/31/02	
13. Office Manager Signature/Date Jim Mather /s/ 5/8/02			14. Category of Change: Update ECS Baseline Doc.		15. Priority: (If "Emergency" fill in Block 27). Routine	
16. Documentation/Drawings Impacted: 920-TDL-001			17. Schedule Impact:		18. CI(s) Affected:ACMHW	
19. Release Affected by this Change: N/A		20. Date due to Customer:		21. Estimated Cost: None - Under 100K		
22. Source Reference: <input type="checkbox"/> NCR (attach) <input type="checkbox"/> Action Item <input type="checkbox"/> Tech Ref. <input type="checkbox"/> GSFC <input type="checkbox"/> Other:						
23. Problem: (use additional Sheets if necessary) The 2x168 MHz CPU board which was installed in I0dis01 was recently moved by LDAAC to I0dis02 to support reprocessing testing. CPUs are needed in I0dis01 for Solaris 8 transition						
24. Proposed Solution: (use additional sheets if necessary) Install CPU board (Serial number 5012976061797) in I0dis01. This board was formerly installed in g0dis01.						
25. Alternate Solution: (use additional sheets if necessary) none						
26. Consequences if Change(s) are not approved: (use additional sheets if necessary) Increased technical risk for Solaris 8 transition resulting from the need to upgrade I0dis02 in place.						
27. Justification for Emergency (If Block 15 is "Emergency"):						
28. Site(s) Affected: <input type="checkbox"/> EDF <input type="checkbox"/> PVC <input type="checkbox"/> VATC <input type="checkbox"/> EDC <input type="checkbox"/> GSFC <input checked="" type="checkbox"/> LaRC <input type="checkbox"/> NSIDC <input type="checkbox"/> SMC <input type="checkbox"/> AK <input type="checkbox"/> JPL <input type="checkbox"/> EOC <input type="checkbox"/> IDG Test Cell <input type="checkbox"/> Other						
29. Board Comments:			30. Work Assigned To:		31. CCR Closed Date:	
32. EDF/SCDV CCB Chair (Sign/Date):		Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS				
33. M&O CCB Chair (Sign/Date): Gary Gavigan /s/ 5/21/02		Disposition: <u>Approved</u> App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS				
34. ECS CCB Chair (Sign/Date):		Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ESDIS				

